

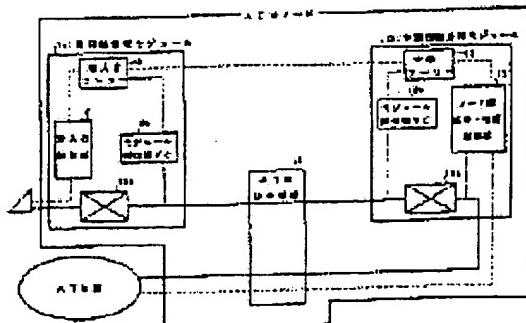
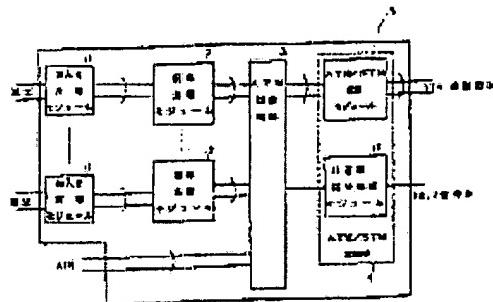
## ATM NODE SYSTEM

**Patent number:** JP4180324  
**Publication date:** 1992-06-26  
**Inventor:** NOTOYA ATSUSHI; ISHIDA HIROSHI  
**Applicant:** OKI ELECTRIC IND CO LTD  
**Classification:**  
- international: (IPC1-7): H04L12/48  
- european:  
**Application number:** JP19900307213 19901115  
**Priority number(s):** JP19900307213 19901115

[Report a data error here](#)

### Abstract of JP4180324

**PURPOSE:** To attain the simplification of the configuration of a communication processing module and the facilitation of the perfect conversion to an ATM node system by installing an ATM/STM converting part between the communication processing module and an STM node, and executing mutual conversion between an ATM and an STM. **CONSTITUTION:** For instance, in the case of the execution of call processing between the ATM/ATM nodes, a call control signal and user information are transferred from an originating communication processing module 2a to a repeating communication processing module 2b. Besides, in the case of the execution of the call processing between the ATM/STM(asynchronous/ synchronous transfer mode) nodes, the call control signal and the user information are transferred from the communication processing module 2 to an ATM/ STM conversion module 5, and further, STM user information is connected to an STM communication channel network. Then, the call control signal is connected to a CCITT No.7 signal network through a common channel signal processing module 6. Accordingly, the communication processing module needs only to execute the same call processing whether connection is between the ATM/ATM nodes or between the ATM/STM nodes.



Data supplied from the [esp@cenet](mailto:esp@cenet) database - Worldwide

**BEST AVAILABLE COPY**